

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : James J. Riches
For : AN OFFSET, DELAY AND PARASITICALLY IMMUNE
RESISTER-CAPACITOR (RC) TRACKING LOOP AND
METHOD OF USING SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

UNDER 37 CFR §1.97(b)

Pursuant to 37 CFR §§1.56 and 1.97(b), Applicant brings to the attention of the Examiner the documents listed on the attached Form PTO 1449. This Information Disclosure Statement is being filed with the above referenced application.

Applicant has enclosed copies of the publications listed on the attached Form PTO 1449.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed documents are material or constitute "prior art." If it should be determined that a listed document does not constitute "prior art" under United States law, Applicant reserves the right to present to the Office the relevant facts and law regarding the appropriate status of such document.

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Atty. Docket : SC12661J
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Applicant further reserves the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should the documents be applied against the claims of the present application.

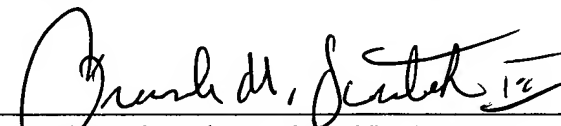
Respectfully submitted,

JAMES J. RICHES

By: MILLER, JOHNSON, SNELL & CUMMISKEY, P.L.C.

Date

12/30/03



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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	
		Filing Date	
		First Named Inventor	James J. Riches
		Art Unit	
		Examiner Name	
Sheet 1	of 1	Attorney Docket Number	SC12661J

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	✓	LINDFORS, SASKA et al., "A 3-V Continuous-Time Filter with On-Chip Tuning for IS-95, IEEE Journal of Solid-State Circuits, Vol. 34, No. 8, August 1999, pp. 1150-1154.	
		HALONEN, KARI et al., "A 3V gm C-Filter with On-Chip Tuning for CDMA, IEEE 1997 Custom Integrated Circuits Conference, 1997, pp. 5.6.1-5.6.4.	
		SIMON, M. K., "The True Performance of the Simplified Data-Transition Tracking Loop, TDA Progress Report 42-31, November 15, 1997, pp. 1-9	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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